

FORM PTO-1449 U.S. Department of Commerce
(Rev. 4/92) Patent and Trademark Office

ATTY. DOCKET NO.

SERIAL NO.

500.31108CC5

09/712,171

APPLICANT

A. TANAKA et al.

FILING DATE

November 15, 2000

GROUP

2184

INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(Use several sheets if necessary)

U.S. PATENT DOCUMENTS

[illegible]

FOREIGN PATENT DOCUMENTS

[illegible]

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

AB		W. Meador, "Disk Array Systems", <u>Digest of Papers, Compcon '89: Thirty-Fourth IEEE Computer Society International Conference</u> , February 27, 1989, San Francisco, CA, pp. 143-146, IEEE, Piscataway, NJ, ISBN 0-8186-1909-0.

EXAMINER

Scott Bademan

DATE CONSIDERED

12/9/02

EXAMINER: Initial if citation is considered, draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

FORM PTO-1449 U.S. Department of Commerce
(Rev. 4/92) Patent and Trademark Office

ATTY. DOCKET NO.

SERIAL NO.

500.31108CC5

09/712,171

APPLICANT

A. TANAKA et al.

FILING DATE

November 15, 2000

GROUP

2184

INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(Use several sheets if necessary)

U.S. PATENT DOCUMENTS

[illegible]

FOREIGN PATENT DOCUMENTS

[illegible]

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

MS	G. Gibson et al., "Failure Correction Techniques for Large Disk Arrays", <u>Proceedings of the Third International Conference on Architectural Support for Programming Languages and Operating Systems (ASPLOS-III)</u> , April 3-6, 1989, Boston, MA, pp. 123-132, ACM Press, New York, NY, ISBN 0-89791-300-0.

EXAMINER

Scott Baderman

DATE CONSIDERED

12/9/02

EXAMINER: Initial if citation is considered, draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

FORM PTO-1449 U.S. Department of Commerce
(Rev. 4/92) Patent and Trademark Office

ATTY. DOCKET NO.

SERIAL NO.

500.31108CC5

09/712,171

APPLICANT

A. TANAKA et al.

FILING DATE

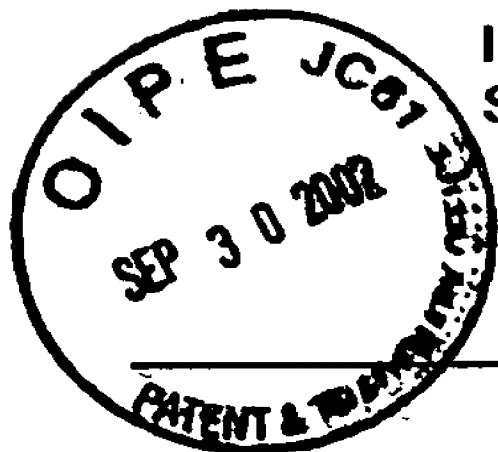
November 15, 2000

GROUP

2184

INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(Use several sheets if necessary)




U.S. PATENT DOCUMENTS

[illegible]

FOREIGN PATENT DOCUMENTS

[illegible]

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

		<p>T. Olson, "Disk Array Performance in a Random IO Environment", <u>ACM SIGARCH Computer Architecture News</u>, Vol. 17, No. 5, September 1989, pp. 71-77, ACM Press, New York, NY, ISSN 0163-5964.</p>
		<p>P. Chen et al., "Maximizing Performance in a Striped Disk Array", <u>Proceedings of The 17th Annual International Symposium on Computer Architecture</u>, May 28-31, 1990, Seattle, WA, pp. 322-331, IEEE Computer Society Press, Los Alamitos, CA.</p>

EXAMINER

Scott Baderman

DATE CONSIDERED

12/9/02

EXAMINER: Initial if citation is considered, draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.